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ARMv7-M Architecture Reference Manual, ARM DDI0403D, ARM Ltd. Cortex Microcontroller Software Interface Standard (see Www.onarm.com). Application Note 179 4th, 2024

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SPSR (status Registers). In Addition, There Are Synonyms Defined For The General-purpose Registers R0-R15 Based On Their Special Use ([1], [3]): • R0 - R3 Are Also Called A1 - A4, As A Synonym For Argument Registers, As These 1th, 2024

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