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8086/8088 Hardware Specifications - WordPress.com • Write Line Indicates 8086/8088 Is Outputting Data To A Memory Or I/O Device. -during The Time WR Is A Logic 0, The Data Bus Contains Valid Data For Memory Or I/O -high-impedance During A Hold Acknowledge INTA • The Interrupt Acknowledge Signal Is A Response To The INTR Input Pin. -normally Used To Gate The Interrupt Vector Number 3th, 2024 Microcomputer Systems The 8086 8088 Family Architecture Microcomputer Systems The 8086/8088 Family Architecture Programming And Design Author (S) Yu-Cheng Liu Glenn A. Gibson Publication Data Englewood Cliffs: Prentice Hall

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