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Modeling And Extraction Of Nanometer Scale Interconnects ... Proceedings Of The 23rd Advanced Metallization Conference (AMC), San Diego, CA, October 16-19, 2006. Figure 1: Interconnect Schematic Showing Parameters For Capacitance Modeling. In 2D, The Capacitance Between Conductors B And C Is Independent Of The Conductor A. 2 Jun 4th, 2024

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