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FET Small-Signal Analysis FET SSAC Analysis Steps
1. Draw The SSAC Equivalent Circuit
A) Draw The AC Equivalent Circuit (signal Frequency Is In Ni, yt I.e., $F = 1$)
i. Capacitors Are Short Circuit, I.e., $X_C \rightarrow 0$.
ii. Kill The DC Power Sources (i.e., AC Value Of DC Sources Is Zero).
B) Replace FET With Its Small-signal Equivalent Model. 4th, 2024

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1.0 Mohm Is The Minimal Acceptable Input Impedance

For A Guitar Device. Medium (6.0 Kohm) Output Impedance. There's No Need For An Especially Low Output Impedance As We're Only Using The Preamp To Counter The Effects Of Cable Capacitance And Loading. 3 DB Gain. You Don't Need Much Gain, And Given T
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