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CMOS Families In Section 3.8. We'll Use The Following Definitions Of LOW And HIGH In Our Discussions Of TTL Circuit Behavior: LOW 0–0.8 Volts. HIGH 2.0–5.0 Volts. TTL.1 Basic TTL NAND Gate The Circuit Diagram For A 2-input LS-TTL NAND Gate, Part Number 74LS00, Is Shown In Figure 1th, 2024

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In An Input Circuit With An Impedance Of 500 Ohms, There Would Be A 0.5-volt Drop. If That Same 1 Milliamp Were Trans Ferred (Assuming No Losses In The Transistor) To A 10,000-ohm Output Impedance, The Voltage Drop Would Be 10 Volts-or, In Other Words, A Gain Of 20 Volts In The Circuit 1th, 2024

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