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2024Chapter 8 Memory Hierarchy And Cache Memory• Suppose Processor Has 2  
Levels Of Hierarchy: Cache And Main Memory • T Cache = 1 Cycle, T MM = 100  
Cycles • What Is The AMAT Of The Program From Example 1?  $AMAT = T_{Cache} +$   
 $MR_{Cache} (t_{MM}) = [1 + 0.375(100)] \text{ Cycles} = 38.5 \text{ Cycles}$  Memory Performance

Example 2 Feb 2th, 2024.

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Sophiatown Would Be Reborn" (Mattera, 2009, P. 152). Mattera Would Probably Be  
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