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Schematic Diagram Driver Trans Power Ampl Antenna Circuit (trans-rec) _ 3 _e C101~ Im 1 5 4!t 7 — - — V1' N O~ C-4~)i Iv1 I C-40 Oi I' ~p 1 100 - 1 Nc-42 J L•19 Cj4 30~ 2 U Lsl- I L-~75_----- I 4th, 2024JOINT TRANSMITTER / RECEIVER DESIGN FOR MULTICARRIER ...F 0 Y()m \hat{u} ()m $\hat{u}0 \hat{u}1 \hat{u}M$ -1 V (z) (z)-1 YP-1 Y0 Y1 G(z) Z (z)-1C F(z) L Zeros {Figure 3: Smith Decomposition Of Channel Matrix; Special Structure Of Transmitter And Receiver The Assumptions That Both, Transmitter And Receiver, Are FIR Redunda 3th, 2024.

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