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Can Use TSVs [1] Are As An Option To Wire-bond And flip Chips To Form 3D Packages And 3D Integrated Circuits. The Reason To Accomplish Broad Interconnectedness And Also To Attain Larger Space Efficiencies In 2th, 2024.

Optimization Of Test Wrapper For TSV Based 3D SOCs3D SOCs Using Optimum Number Of TSVs Available For Testing. The Authors In [2] Have Proposed A Heuristic Algo-rithm To Minimize The Number Of The TSVs Within The Wrap-per Chains By Mapping 3D Wrapper Design Problem Into 2D. In This Work, We Are Mainly Trying To Focus On Wrapper Design And Optimization For The Cores Of 3D SOC Where 3th, 2024/data/Content/pdf Wrapper/DanM/input/ED060020Dance Overture.

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