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Condition Mainly Used With Binary Logic Elements Where The Logic State 1 (TRUE) Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2] 3.20 Logic Inversion Condition Mainly Used With Binary Logic Elements Where A Higher Physical Level Is Converted To A Lower Physical Level Or Vice Versa [ Mar 3th, 2024

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Assembly Language Program For Addition Of Two 8-bit
Numbers And Sum Is 8 Bit. Instruments Required: 1.
8085 Microprocessor Kit . 2. +5V Power Supply .
Theory: Consider The First Number 26H Is Stored In
Memory Location 8000H And The Second Number 62H
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### 8085 Microprocessor Kit User's Manual - Kswichit.com

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