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References FE/EIT Review
FE/EIT Review Circuits Instructor: Russ Tatro 4/5/2010 2 References John A. Camara, Electrical Engineering Reference Manual, 6 Th Edition, Professional Publications, Inc, 2002. John A. Camara, Practice Problems For The Electrical And Computer Engineering PE Exam, 6th Edition, Professional Publications, Inc, 2002. File Size: 309KB May 2th, 2024
FE/EIT Review References
Flowchart The Following Procedure: If A Is Greater Than 10 And If A Is Less Than 14, Subtract X From A. If A Is Less Than 10 Or If A Is Greater Than 14, Exit The Program. All Flow Charts Start With A Terminal And End With A Terminal. Use An Input Symbol To Enter A Use A Decision Symbol The Compare A To Preset Values. Feb 3th, 2024
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Saturating The Op Amp At Around $\pm 10-15$ V. The Input Impedance Of An Op Amp Circuit Is The Ratio Of The Applied Voltage To Current Drawn (v_{in}/i_{in}). In Practical Circuits, The Input Impedance Is Determined By Assuming That The Op Amp Itself Draws No Current; Any Current Drawn Is Assumed To May 2th, 2024.

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Course Discipline Specific Core
Q.1. Five Objective Type Questions Each Of 2 Marks, I.e. $5 \times 2 = 10$ Marks. Q.2, Q.3 And Q.4 Are Compulsory, Each Of 10 Marks, I.e. $3 \times 10 = 30$ Marks
Module B (40 Marks) Q.5.: Compulsory Question Of 10 Marks Containing Objective Questions Any 3 Questions Of 10 Marks Each From Q.6-Q.9. No Part Will Be More Than 5 Marks. Feb 1th, 2024

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