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Swedish Center For III-Nitride Technology

R. Delgado Carrascon Et Al., "Optimization Of GaN Nanowires Reformation Process By MOCVD For Device-Quality GaN Templates", Physica Status Solidi B 257, 1900581 (2020) D.-Y. Chen Et Al., "Microwave Performance Of 'Buffer-Free'GaN-on-SiC High Electron Mobility Transistors," IEEE Electron Dev. Lett. 41, 828 (2020) Apr 2th, 2024

Linux Device Drivers, 2nd Edition - NXP Semiconductors

On The Technical Side, This Text Should Offer A Hands-on Approach To Understanding The Kernel Internals And Some Of The Design Choices Made By The Linux Develop-ers. Although The Main, Official Target Of The Book Is Teaching How To Write Device Drivers, The Material Should Give An Interesting Overview Of The Kernel Implementa-tion As Well. Although Real Hackers Can find All The Necessary ... Jan 2th, 2024

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Ultrafast Magneto-optics In Ferromagnetic III-V Semiconductors Jigang Wang 1,6, Chanjuan Sun1, Yusuke Hashimoto 7, Junichiro Kono1,8, ... Introduction 1.1. Motivation: Spin-related Phenomena And Devices Over The Past Several Years There Has Been Considerable Interest And Progress In Exploring May 2th, 2024

III-V Ferromagnetic Semiconductors

1 III-V Ferromagnetic Semiconductors F. Matsukuraa,b), H. Ohnoa), And T. Dietla,b) A) Laboratory For Electronic Intelligent Systems, Research Institute Of Electrical Communication,

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Lessons In Electric Circuits, Volume III { Semiconductors By Tony R. Kuphaldt Fourth Edition, Last Update January 18, 2006 Mar 2th, 2024

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Containing Simultaneously Al(III), Ga(III), In(III), III

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