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LECTURE 02 - SUBMICRON CMOS TECHNOLOGY

TYPICAL SUBMICRON CMOS FABRICATION PROCESS N-Well CMOS Fabrication Major Steps 1.) Implant And Diffuse The N-well 2.) Deposition Of Silicon Nitride 3.) N-type Field (channel Stop) Implant 4.) P-type Field (channel Stop) Implant 5.) Grow A Thick Field Oxide ... Jul 2th, 2024

LECTURE 03 - DEEP SUBMICRON (DSM) CMOS TECHNOLOGY

Major Fabrication Steps For A DSM CMOS Process 1.) P And N Wells 2.) Shallow Trench Isolation 3.) Threshold Shift And Anti-punch Through Implants 4.) Thin Oxide And Gate Polysilicon 5.) Lightly Doped Drains And Sources 6.) Sidewall Spacer 7.) Heavily Doped Drains And Sources May 1th, 2024
Introduction To Deep Submicron CMOS Device Technology ... • CMOS Technology Trends • MOSFET Basics • Deep Submicron FET Fabrication Sequence • Enabling Technologies • Second-Order Consequences • Dealing With Process Variations In Manufacturing • Conclusions Disclaimer • A Proper Introduction Alone Would Take Weeks, Let Alone A Whole Semester Apr 2th, 2024.

Chapter 4 Low-Power VLSI Design

Power VLSI Design Overview Of Power Consumption • The Average Power Consumption Can Be Expressed As $1 \text{ Avg C Load } V_{DD} C \text{ Load } V_{DD} F_{CLK} T_P / 2$ • The Node Transition Rate Can Be Slower Than The Clock Rate. To Better Represent This Behav Jul 1th, 2024
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