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M. Krishna Kumar MAM/M8/LU18/V1/2004 7 Signal Descriptions Of 80386 (cont..) •CLK2 :The Input Pin Provides The Basic System Clock Timing For The Operation Of 80386. •D0 -D31:These 32 Lines Act As Bidirectional Data Bus During Different Access Cycles. •A31 -A2: These Are Upper 30 Bit Of The 32- Bit Address Bus. •BE 1th, 2024

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